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2834-0133P

TECHNOLOGY CENTER 2800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: HANS GUDE GUDESEN et al

Serial No.: 09/978,043

Group:

Filed: OCTOBER 17, 2001

Examiner:

For: A METHOD FOR GENERATING ELECTRICAL CONDUCTING OR SEMICONDUCTING STRUCTURES IN TWO OR THREE DIMENSIONS, A METHOD FOR ERASING THE SAME STRUCTURES AND AN ELECTRIC FIELD GENERATOR/MODULATOR FOR USE WITH THE METHOD FOR GENERATION

REQUEST FOR A CORRECTED OFFICIAL FILING RECEIPT

Honorable Commissioner of Patents  
and Trademarks  
Washington, D.C. 20231

MAR 21 2002

Sir:

Attached hereto is the Official Filing Receipt in connection with the above-identified application.

THE FOLLOWING CORRECTION(S) IS/ARE RESPECTFULLY REQUESTED:

FOREIGN APPLICATION(S) --

Change From: "NORWAY 19982518 FEBRUARY 6, 1998"

To: --NORWAY 19982518 JUNE 2, 1998--  
TITLE--

Change From: "METHOD FOR GENERATING ELECTRICALLY CONDUCTING AND/OR SEMICONDUCTING STRUCTURES IN TWO OR THREE DIMENSIONS, A METHOD FOR ERASING THE SAME STRUCTURES AND AN ELECTRIC FIELD GENERATOR/MODULATOR FOR USE WITH THE METHOD FOR GENERATING

To: --A METHOD FOR GENERATING ELECTRICAL CONDUCTING OR SEMICONDUCTING STRUCTURES IN TWO OR THREE DIMENSIONS, A METHOD FOR ERASING THE SAME STRUCTURES AND AN ELECTRIC FIELD GENERATOR/MODULATOR FOR USE WITH THE METHOD FOR GENERATION--

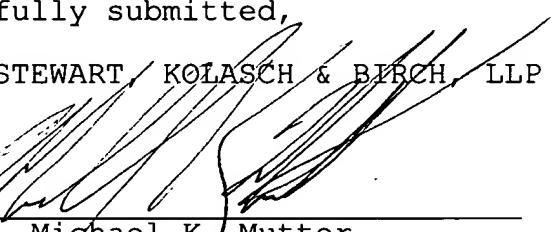
09/978,043

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. 1.16 or under 37 C.F.R. 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By: \_\_\_\_\_

  
Michael K. Mutter  
Reg. No. 29,680

P.O. Box 747  
Falls Church, VA 22040-0747

Attachment  
MKM/srw  
(703)205-8000

Preliminary Class

438

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**LICENSE FOR FOREIGN FILING UNDER  
Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

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**NOT GRANTED**

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PLEASE NOTE:  
YOU MUST  
COMPLETE THE  
FOLLOWING:

COMBINED DECLARATION AND POWER OF ATTORNEY  
FOR PATENT AND DESIGN APPLICATIONS

ATTORNEY DOCKET NO.  
2834-112P

Insert Title:

As a below named inventor, I hereby declare that: my residence, post office address and citizenship are as stated next to my name; that I verily believe that I am the original, first and sole inventor (if only one inventor is named below) or an original, first and joint inventor (if plural inventors are named below) of the subject matter which is claimed and for which a patent is sought on the invention entitled: A method for generating electrical conducting or semiconducting structures in two or three dimensions, a method for erasing the same structures and an electric field generator/modulator for use with the method for generation the specification of which is attached hereto. If not attached hereto,

Fill in Appropriate  
Information  
For Use Without  
Specification  
Attached:

the specification was filed on \_\_\_\_\_ as

United States Application Number \_\_\_\_\_; and / or

the specification was filed on 28 January 1999 as PCT

International Application Number PCT/N099/00022; and was

amended under PCT Article 19 on \_\_\_\_\_ (if applicable)

**COPY**

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56.

I do not know and do not believe the same was ever known or used in the United States of America before my or our invention thereof, or patented or described in any printed publication in any country before my or our invention thereof or more than one year prior to this application, that the same was not in public use or on sale in the United States of America more than one year prior to this application, that the invention has not been patented or made the subject of an inventor's certificate issued before the date of this application in any country foreign to the United States of America on an application filed by me or my legal representatives or assigns more than twelve months (six months for designs) prior to this application, and that no application for patent or inventor's certificate on this invention has been filed in any country foreign to the United States of America prior to this application by me or my legal representatives or assigns, except as follows.

I hereby claim foreign priority benefits under Title 35, United States Code, §119 (a)-(d) of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Insert Priority  
Information:  
(if appropriate)

► Prior Foreign Application(s)

Priority	Claimed
<input checked="" type="checkbox"/>	<input type="checkbox"/>
Yes	No
<input checked="" type="checkbox"/>	<input type="checkbox"/>
Yes	No
<input type="checkbox"/>	<input type="checkbox"/>
Yes	No
<input type="checkbox"/>	<input type="checkbox"/>
Yes	No
<input type="checkbox"/>	<input type="checkbox"/>
Yes	No

I hereby claim the benefit under Title 35, United States Code, §119(e) of any United States provisional application(s) listed below.

Insert Provisional  
Application(s):  
(if any)

Application Number \_\_\_\_\_ (Filing Date)  
Application Number \_\_\_\_\_ (Filing Date)

Insert Requested  
Information:  
(if appropriate)

All Foreign Applications, if any, for any Patent or Inventor's Certificate Filed More Than 12 Months (6 Months for Designs) Prior To The Filing Date of This Application:

Country \_\_\_\_\_ Application No. \_\_\_\_\_ Date of Filing (Month Day/Year) \_\_\_\_\_

Insert Prior U.S.  
Application(s):  
(if any)

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37, Code of Federal Regulations, §1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

Application Number \_\_\_\_\_ (Filing Date) \_\_\_\_\_ (Status - patented, pending, abandoned)  
Application Number \_\_\_\_\_ (Filing Date) \_\_\_\_\_ (Status - patented, pending, abandoned)



UNITED STATES PATENT AND TRADEMARK OFFICE

COMMISSIONER FOR PATENTS  
UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231  
www.uspto.gov

APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/978,043	10/17/2001	2813	740	2834-0133P	10	13	2

**CONFIRMATION NO. 4358**

2292  
BIRCH STEWART KOLASCH & BIRCH  
PO BOX 747  
FALLS CHURCH, VA 22040-0747

**FILING RECEIPT**



\*OC00000007317449\*

Date Mailed: 01/16/2002

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

**Applicant(s)**

Hans Gude Gudsen, Bruxelles, BELGIUM;  
Per-Erik Nordal, Asker, NORWAY;  
Geirr I. Leistad, Sandvika, NORWAY;

**Domestic Priority data as claimed by applicant**

THIS APPLICATION IS A DIV OF 09/381,994 09/28/1999  
WHICH IS A 371 OF PCT/NO99/00022 01/28/1999

**Foreign Applications**

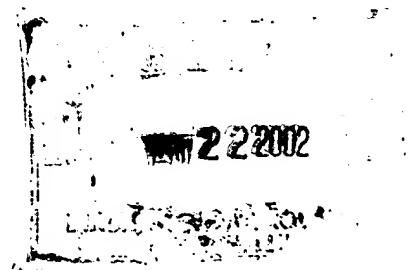
NORWAY 19980385 01/28/1998  
NORWAY 19982518 02/06/1998

If Required, Foreign Filing License Granted 01/16/2002

Projected Publication Date: 04/25/2002

Non-Publication Request: No

Early Publication Request: No



**Title**

Method for generating electrically conducting and/or semiconducting structures in two or three dimensions, a method for erasing the same structures and an electric field generator/modulator for use with the method for generating